Inductance Model of Interdigitated Power and Ground Distribution Networks

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Abstract—A closed-form expression is presented in this brief to accurately estimate the effective inductance of a single layer within an interdigitated power and ground (P/G) distribution network. Due to the large number of P/G lines in these networks, excessive time is required to calculate the inductance using 3-D simulation tools. The proposed expression is favorably compared with previous models and FastHenry, exhibiting accuracy and computational efficiency. The inductance of a single layer within an interdigitated P/G distribution network is bounded for any number of lines. The error of the proposed expression rapidly decreases with an increasing number of pairs within the network. The upper bound for the error of the proposed model is also determined.

Index Terms—Interdigitated structure, power and ground (P/G) network, mutual inductance, self-inductance.

I. INTRODUCTION

W ITH high operating frequencies and scaled geometries, power and ground (P/G) distribution networks require greater design optimization to effectively provide higher current flow. Low supply voltages and high currents in ICs place stringent constraints on P/G distribution networks. Higher frequencies and smaller transistors produce shorter transition times, such that L(di/dt) voltage drops exceed *IR* voltage drops. All of these factors require the inductance to be considered in the design of on-chip P/G distribution networks. To optimize these large-scale P/G distribution networks, the inductance needs to be accurately and efficiently calculated.

An interdigitated P/G distribution network structure, where a few wide lines are replaced by a large number of narrow lines, is often used to reduce the inductance effect [1], [2]. Different P/G structures have been compared in [3], where the interdigitated structure is shown to achieve the greatest reduction in inductive effects. An interdigitated structure has also been applied to clock networks in [4].

Each layer of a multiplane P/G distribution network consists of interdigitated P/G lines. The direction of the wires is perpendicular to the direction of the wires in the previous layer, as shown in Fig. 1, ensuring no inductive coupling between the layers. The advantages of an interdigitated structure are in-

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Fig. 1. Interdigitated P/G distribution network. The darker and lighter lines represent the power and ground lines, respectively.



Fig. 2. Four pairs of a single layer within an interdigitated P/G distribution network.

creased routing flexibility and reduced inductance effects. The current flow of the P/G lines within a layer is assumed to flow in opposite directions, thereby reducing the loop inductance of the network [5]. The loop inductance of two parallel wires with opposite current flow is

$$L_{\rm loop} = L_{11} + L_{22} - 2M_{12} \tag{1}$$

where L_{11} , L_{22} , and M_{12} are the self-inductance of the P/G lines and the mutual inductance between these two wires, respectively.

The process of estimating the inductance becomes problematic with a large number of wires. To calculate the loop inductance, the mutual inductance terms among all of the wires need to be individually determined, which is a computationally expensive process. A closed-form expression characterizing this inductance would therefore be useful.

The inductance of a single layer within an interdigitated P/G distribution network structure with four pairs (eight wires), as shown in Fig. 2, is

$$\frac{1}{L_{\text{eff}}} = \frac{1}{L_1} + \frac{1}{L_2} + \frac{1}{L_3} + \frac{1}{L_4}$$
(2)

where L_1 , L_2 , L_3 , and L_4 are the inductance of the first, second, third, and fourth pairs of a single layer within a P/G distribution network, respectively. The inductance of the first pair is

$$L_{1} = L_{1_{p}} + L_{1_{g}} + \sum_{i=2}^{4} (M_{1_{p}i_{p}} + M_{1_{g}i_{g}}) - \sum_{i=1}^{4} (M_{1_{p}i_{g}} + M_{1_{g}i_{p}}) \quad (3)$$

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Fig. 3. n pairs of a P/G distribution network. The focus of (10) is on the effective inductance of pair m.

where subscripts p and g represent power and ground, respectively. In this case, the overall inductance requires sixteen terms to determine each pair. For n pairs of P/G distribution networks, $2 \cdot 2n = 4n$ terms are required to characterize each pair, making complexity O(n) for a single pair. For n pairs, the complexity in estimating the inductance of a single layer within a P/G network is $O(n^2)$.

The brief is organized as follows: A closed-form expression of the self- and mutual inductance of an interdigitated P/G distribution network is presented in Section II. The accuracy of this expression and a comparison to other models are provided in Section III. The upper bound for the error of the proposed model is also provided. The brief is concluded in Section IV.

II. INDUCTANCE OF P/G DISTRIBUTION NETWORK

The definition of the inductance between two loops i and j for a uniform current density is presented by the Neumann equation

$$L_{ij} \equiv \frac{\mu_0 \mu_r}{4\pi} \oint_{C_i} \oint_{C_j} \frac{ds_i ds_j}{|R_{ij}|} \tag{4}$$

where μ_0 , μ_r , and R_{ij} are the vacuum and relative permeability, and the distance between two loops, respectively. From [6], the mutual inductance between a pair of two rectangular conductors is

$$M = \frac{\mu_0 l}{2\pi} \left[\ln\left(\frac{l}{d} + \sqrt{1 + \frac{l^2}{d^2}}\right) - \sqrt{1 + \frac{d^2}{l^2}} + \frac{d}{l} \right]$$
(5)

where l and d are the length of the wire and the pitch of two wires, respectively. If $l \gg d$, an approximate expression based on a Taylor series expansion is [7]

$$M = \frac{\mu_0 l}{2\pi} \left[\ln \left(\frac{2l}{d} \right) - 1 + \frac{d}{l} \right].$$
 (6)

The self-inductance is derived in a similar way. For those cases where the length is larger than the width [8]

$$L_s = \frac{\mu_0 l}{2\pi} \left[\ln\left(\frac{2l}{w+t}\right) + \frac{1}{2} + \frac{k(w+t)}{l} \right] \tag{7}$$

where w, t, and k are the wire width, wire thickness, and fitting parameter $(k \approx 0.22)$ for smaller length wires, respectively. In P/G distribution networks where $l \gg d$ and $l \gg w + t$, the last term characterizing the edge effect of the self- and mutual inductance can be neglected, simplifying (6) and (7) to

$$M = \frac{\mu_0 l}{2\pi} \left[\ln \left(\frac{2l}{d} \right) - 1 \right] \tag{8}$$

$$L_s = \frac{\mu_0 l}{2\pi} \left[\ln\left(\frac{2l}{w+t}\right) + \frac{1}{2} \right] \tag{9}$$

respectively. The mutual component of the inductance within an interdigitated P/G distribution network decreases with increasing distance between the wires and can be treated as a local effect, according to [5]. In this case, the effective inductance of each pair is the sum of the self-inductances and a single mutual inductance between the two wires in the pair. This approach supports fast estimation of the effective inductance of a P/G distribution network but suffers in accuracy since the mutual inductance terms between all other parallel wires are neglected. Enhanced accuracy in estimating the mutual inductance terms is required.

The effective inductance of an arbitrary pair of P/G lines m within an interdigitated P/G distribution network is shown in Fig. 3 and is

$$L_m = 2L_{m_s} - 2M_{m_p m_g} + \sum_{\substack{i=1\\i\neq m}\\i\neq m}^n \times (M_{m_p i_p} - M_{m_p i_g} - M_{m_g i_p} + M_{m_g i_g}).$$
(10)

The terms $M_{m_p i_p} = M_{m_g i_g}$ are equal for any *i* in (10) since the distance between the power lines of pair *m* and *i* and the ground lines of pair *m* and *i* is the same. In addition, (10) can be rewritten as a function of distance d = w + s, where *s* is the spacing

$$\begin{split} L_m &= 2L_{m_s} - 2M(d) \\ &+ \sum_{\substack{i=1\\i\neq m\\k=|m-i|}}^{n} [2M(2dk) - M(2dk-d) - M(2dk+d)] \quad (11) \end{split}$$

where

$$M(x) = \frac{\mu_0 l}{2\pi} \left[\ln\left(\frac{2l}{x}\right) - 1 \right].$$
 (12)

Equation (11) consists of three terms: 1) the self-inductance of two wires; 2) the mutual inductance between these two wires; and 3) the sum of the mutual inductance between all of the other



Fig. 4. Terms of (14). The values quickly decline in magnitude to zero.

wires. The third term is neglected in [5]. Substituting (12) into (11), the summation term is

$$\sum M = \sum_{\substack{i=1\\ i \neq m\\ k = |m-i|}}^{n} \frac{\mu_0 l}{2\pi} \left[2\ln\left(\frac{2l}{2dk}\right) - \ln\left(\frac{2l}{2dk-d}\right) - \ln\left(\frac{2l}{2dk+d}\right) \right]. \quad (13)$$

The sum of the logarithmic terms is the product of a single logarithm, permitting (13) to be expressed as

$$\sum M = \sum_{\substack{i=1\\k=|m-i|\\k=|m-i|}}^{n} \frac{\mu_0 l}{2\pi} \ln\left(\frac{2l}{2dk}\frac{2l}{2dk}\frac{2dk-d}{2l}\frac{2dk+d}{2l}\right)$$
$$= \sum_{\substack{i=1\\k=|m-i|\\k=|m-i|}}^{n} \frac{\mu_0 l}{2\pi} \ln\left(\frac{2dk-d}{2dk}\frac{2dk+d}{2dk}\right)$$
$$= \frac{\mu_0 l}{2\pi} \sum_{\substack{i=1\\k=|m-i|\\k=|m-i|}}^{n} \ln\left(\frac{2k-1}{2k}\frac{2k+1}{2k}\right).$$
(14)

P/G distribution networks typically consist of a large number of interdigitated pairs, and, as shown in Fig. 4, the terms of (14) quickly decline in magnitude to zero. The number of pairs on the left (and right) is therefore assumed to be infinite, permitting (14) to be formulated as

$$\sum M = \frac{\mu_0 l}{2\pi} \lim_{n \to \infty} \left[2 \sum_{k=1}^n \ln\left(\frac{2k-1}{2k} \frac{2k+1}{2k}\right) \right].$$
 (15)

The factor of two originates from the two sides of the target pair. The infinite sum of (15) is presented as an infinite product

$$\sum M = 2\frac{\mu_0 l}{2\pi} \ln \left[\lim_{n \to \infty} \prod_{k=1}^n \left(\frac{2k-1}{2k} \frac{2k+1}{2k} \right) \right]$$
$$= 2\frac{\mu_0 l}{2\pi} \ln \left[\lim_{n \to \infty} \prod_{k=1}^n \left(1 - \frac{1}{(2k)^2} \right) \right].$$
(16)

The limit of the product can be solved using the Wallis formula [9]

$$\frac{\sin(x)}{x} = \prod_{n=1}^{\infty} \left(1 - \frac{x^2}{\pi^2 n^2} \right)$$
(17)

at $x = \pi/2$, leading to the equality

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$$\lim_{n \to \infty} \prod_{k=1}^{n} \left(1 - \frac{1}{(2k)^2} \right) = \frac{2}{\pi}.$$
 (18)

Based on (18), (10) may be presented in closed form as

$$L_m = 2\frac{\mu_0 l}{2\pi} \left[\ln\left(\frac{2l}{w+t}\right) + \frac{1}{2} - \ln\left(\frac{2l}{d}\right) + 1 + \ln\left(\frac{2}{\pi}\right) \right]$$
$$= 2\frac{\mu_0 l}{2\pi} \left[\ln\left(\frac{d}{w+t}\right) + \frac{3}{2} + \ln\left(\frac{2}{\pi}\right) \right]. \tag{19}$$

To estimate the overall inductance of a structure with N P/G line pairs, the inductance of each pair is assumed to be equal. The mutual inductance between all of the other P/G pairs converges to a constant, making the inductance independent of the number of P/G pairs. The error is greatest in those cases where the number of pairs is smallest; however, in these cases, the effective inductance can quickly be determined with no approximation due to the small number of pairs. For those cases where the number of pairs is sufficiently large (eight pairs produce less than 10% error), the effective inductance is

$$L_{\text{eff}} = \frac{2}{N} \frac{\mu_0 l}{2\pi} \left[\ln\left(\frac{d}{w+t}\right) + \frac{3}{2} + \ln\left(\frac{2}{\pi}\right) \right].$$
(20)

Note than the effective inductance is described for a single layer within an interdigitated P/G network, where it is assumed that no metal layers are above or below the structure. In practical cases, the existence of different interconnect structures above or below the structure may reduce the accuracy of the proposed model. For structures with large spacing, an interconnect structure below the target structure reduces the accuracy of the estimated inductance [10]. Interdigitated P/G networks, however, are designed with small spacing to exploit the available metal resources; therefore, the accuracy of the effective inductance model is maintained.

Additionally, since the current is assumed to flow throughout the entire interdigitated structure, the inductance determined in (20) represents the worst case effective inductance. Assuming that the current is uniformly distributed throughout the interdigitated structure, the worst case effective inductance produces the largest voltage drop over the P/G distribution network.

III. COMPARISON AND DISCUSSION

Three different models are compared in this section. The Grover model describes the inductance of each pair based on (10), where every mutual component is individually calculated [7]. While the individual inductance of each pair is determined, the effective inductance of a single layer within an interdigitated P/G network structure is estimated, assuming that the individual inductive lines are in parallel. Hence, the Grover model refers to the evaluation of every mutual term among all of the wires

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Fig. 5. Comparison of FastHenry and the Grover, Mezhiba, and proposed models for two different design cases.

in a system. In [5], the effective inductance is determined based on an approximation, where the inductance is treated as a local effect, and the mutual inductance between other pairs is neglected. This model is called the Mezhiba model. The proposed model, as represented by (20), determines the effective inductance, assuming that the number of P/G pairs is infinite. Since the magnitude of the mutual terms quickly declines to zero as a function of distance, this assumption is highly accurate.

A comparison among FastHenry [11] (which is a multipole 3-D inductance extraction program) and the Grover, Mezhiba, and proposed models is summarized in this section. In addition, the complexity and accuracy of the proposed, Grover, and Mezhiba models are compared. Two different structures of an interdigitated P/G distribution network are evaluated. For both structures, the width and spacing are maintained constant, i.e., $w = 1 \,\mu\text{m}$ and $s = 1 \,\mu\text{m}$; however, the length and thickness are different, i.e., $l_1 = 1 \text{ mm}$, $t_1 = 0.975 \ \mu\text{m}$, and $l_2 = 100 \ \mu\text{m}$, $t_2 = 0.17 \ \mu m$. The thickness values are based on a 65-nm CMOS technology [12] for the top (M8) and bottom (M1) metal layers, respectively. Both cases represent a single layer within a P/G distribution network. In Fig. 5, two structures are extracted using FastHenry and compared with the Grover, Mezhiba, and proposed models. The proposed and Grover models exhibit enhanced accuracy as compared with the Mezhiba model. In Figs. 6 and 7, the accuracy and complexity are evaluated, respectively. The accuracy is evaluated by comparing the results with FastHenry. The complexity of FastHenry is, however, not evaluated since the required number of terms for the simulator is excessively large as compared with the analytic model.

The complexity and error of the Grover, Mezhiba, and proposed models relative to FastHenry are evaluated. The Grover model considers all of the mutual terms and exhibits the lowest error (less than 1% error); however, the complexity of the Grover model drastically increases for a large number of pairs. The complexity of the proposed and Mezhiba models is independent of the number of P/G pairs. The error of the proposed model decreases with a larger number of P/G pairs, whereas the error of the Mezhiba model increases. The highest error (\sim 30%) of the proposed model occurs with the fewest number of pairs, whereas the error of the Mezhiba model is



Fig. 6. Error comparison for the Grover, Mezhiba, and proposed models. All of the models are compared with FastHenry.



Fig. 7. Comparison of complexity of the Grover, Mezhiba, and proposed models.

highest with the greatest number of P/G pairs. Hence, the error of the proposed model can be reduced using the Grover model, which is only computationally efficient for a few P/G pairs.

Assuming that the number of P/G pairs is infinite, the effect of the mutual inductance terms is greater; therefore, the proposed model underestimates the effective inductance. The mutual inductance of only a single pair is considered by the Mezhiba model, overestimating the inductance. The boundary conditions of the effective inductance are determined from the proposed and Mezhiba models. These conditions permit the effective inductance of a single layer within an interdigitated P/G distribution network structure to be determined for any number of P/G line pairs. The boundary conditions for the effective inductance of an interdigitated P/G distribution network structure are therefore determined by the proposed and Mezhiba models

$$\frac{2}{N}\frac{\mu_0 l}{2\pi} \left[\ln\left(\frac{d}{w+t}\right) + \frac{3}{2} + \ln\left(\frac{2}{\pi}\right) \right] \\ \leq L_{\text{eff}}(x) \leq \frac{2}{N}\frac{\mu_0 l}{2\pi} \left[\ln\left(\frac{d}{w+t}\right) + \frac{3}{2} \right] \quad (21)$$

where x represents any number of pairs within a single layer of an interdigitated P/G distribution network.

An expression is derived for the error between the proposed and Grover models. The normalized error is

$$error = \left| \frac{L_{\text{grover}} - L_{\text{proposed}}}{L_{\text{grover}}} \right| = \left| 1 - \frac{L_{\text{proposed}}}{L_{\text{grover}}} \right|.$$
 (22)

Since the Grover model cannot be expressed by a single equation, only the worst case error is determined. An assumption in the proposed model is that the number of interdigitated pairs is infinite; therefore, the error is highest when only a single pair (N = 1) is present, expressing $error_{N=n} \leq error_{N=1}$. For this case, the inductance based on the Grover model is

$$L_{\text{grover}(N=1)} = 2L_s - 2M = \frac{2\mu_0 l}{2\pi} \left[\ln\left(\frac{d}{w+t}\right) + \frac{3}{2} \right].$$
(23)

The inductance based on the proposed model for N = 1 is

$$L_{\text{proposed}(N=1)} = \frac{2\mu_0 l}{2\pi} \left[\ln\left(\frac{d}{w+t}\right) + \frac{3}{2} + \ln\left(\frac{2}{\pi}\right) \right].$$
(24)

Substituting (23) and (24) into (22), the error bound is

$$ErrorBound_{N\geq 1} = \frac{\ln\left(\frac{\pi}{2}\right)}{\ln\left(\frac{d}{w+t}\right) + \frac{3}{2}}.$$
 (25)

Based on the parameters of width, spacing, and thickness provided earlier in this brief, the *ErrorBound* is less than 0.3 or 30%. The error of the proposed model drastically decreases with a higher number of pairs, as shown in Fig. 6. Similarly, the *ErrorBound* can be expressed for those cases where $N \ge 2$

$$ErrorBound_{N\geq 2} = \frac{\ln\left(\frac{\sqrt{3}}{2}\frac{\pi}{2}\right)}{\ln\left(\frac{d}{w+t}\right) + \frac{3}{2} + \ln\left(\frac{\sqrt{3}}{2}\right)}.$$
 (26)

The *ErrorBound* is less than 0.23 or 23% for those cases where $N \ge 2$ with the aforementioned parameters of width, spacing, and thickness.

IV. CONCLUSIONS

Due to the higher operating frequencies, the role of inductance in the IC design process has become significant. Inductance estimation is a complicated task since every mutual inductance within a system must be considered. An interdigitated P/G distribution network reduces the effective inductance as compared with other P/G distribution network structures. An accurate yet computationally efficient model of this system is desirable to enhance the P/G network design process. The symmetric structure of an interdigitated P/G distribution network supports the development of a closed-form expression to model the effective inductance.

A closed-form expression characterizing the inductance of a single layer within a P/G distribution network has been presented. The solution is compared with previous work and FastHenry, exhibiting good accuracy. The error for the proposed model is highest for a few P/G pairs; however, due to the small number of lines, the Grover model can be used in these cases. With an increasing number of P/G network pairs, the error of the proposed model rapidly decreases, permitting the effective inductance of a P/G distribution network to be accurately and efficiently estimated. The magnitude of the effective inductance is bounded by the values determined from the proposed and Mezhiba models. The bound dramatically decreases with an increasing number of pairs. The effective inductance of a single layer within an interdigitated P/G distribution network structure can therefore be determined for any number of P/G line pairs.

REFERENCES

- D. A. Priore, "Inductance on silicon for sub-micron CMOS VLSI," in Proc. IEEE Symp. VLSI Circuits, May 1993, pp. 17–18.
- [2] L.-R. Zheng and H. Tenhunen, "Effective power and ground distribution scheme for deep submicron high speed VLSI circuits," in *Proc. IEEE Int. Symp. Circuit Syst.*, May 1999, vol. I, pp. 537–540.
- [3] M. Popovich, A. V. Mezhiba, and E. G. Friedman, *Power Distribution Networks With on-Chip Decoupling Capacitors*. New York: Springer-Verlag, 2008.
- [4] Y. Massaud, S. Majors, T. Bustami, and J. White, "Layout techniques for minimizing on-chip interconnect self inductance," in *Proc. IEEE/ACM Des. Autom. Conf.*, Jun. 1998, pp. 566–571.
- [5] A. V. Mezhiba and E. G. Friedman, "Inductance properties of highperformance power distribution grids," *IEEE Trans. Very Large Scale Integr. (VLSI) Syst.*, vol. 10, no. 6, pp. 762–776, Dec. 2002.
- [6] E. B. Rosa, "The self and mutual inductances of linear conductors," Bull. Bureau Standards, vol. 4, no. 2, pp. 301–344, 1908.
- [7] F. Grover, Inductance Calculation: Working Formulas and Tables. New York: Dover, 1962.
- [8] E. B. Rosa and F. W. Grover, Formulas and Tables for Calculation of Mutual and Self-Inductance. Washington, DC: Gov. Printing Office, 1916.
- [9] J. Wallis, Opera Mathematica, Oxonii, Leon: Lichfield Academiæ Typographi, 1656.
- [10] B. Kleveland, X. Qi, L. Madden, T. Furusawa, R. W. Dutton, M. A. Horowitz, and S. S. Wong, "High-frequency characterization of on-chip digital interconnects," *IEEE J. Solid-State Circuits*, vol. 37, no. 6, pp. 716–725, Jun. 2002.
- [11] M. Kamon, M. J. Tsuk, and J. K. White, "FASTHENRY: A multipoleaccelerated 3-D inductance extraction program," *IEEE Trans. Microw. Theory Tech.*, vol. 42, no. 9, pp. 1750–1758, Sep. 1994.
- [12] P. Bai, C. Auth, S. Balakrishnan, M. Bost, R. Brain, V. Chikarmane, R. Heussner, M. Hussein, J. Hwang, D. Ingerly, R. James, J. Jeong, C. Kenyon, E. Lee, S.-H. Lee, N. Lindert, M. Liu, Z. Ma, T. Marieb, A. Murthy, R. Nagisetty, S. Natarajan, J. Neirynck, A. Ott, C. Parker, J. Sebastian, R. Shaheed, S. Sivakumar, J. Steigerwald, S. Tyagi, C. Weber, B. Woolery, A. Yeoh, K. Zhang, and M. Bohr, "A 65 nm logic technology featuring 35 nm gate lengths, enhanced channel strain, 8 Cu interconnect layers, low-k ILD and 0.57 μm² SRAM cell," in *IEDM Tech. Dig.*, Dec. 2004, pp. 657–660.